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APPLICANT: KAWASAKI STEEL CORP;

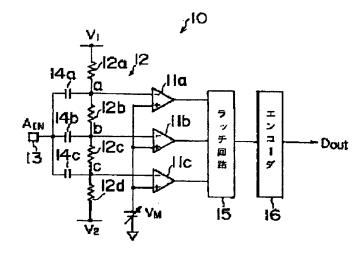
INVENTOR: OGASAWARA HIROSHI;

INT.CL.

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TITLE

: A/D CONVERTER



ABSTRACT :

PURPOSE: To reduce an influence of offset and perform accurate A/D conversion in a short time by giving a simplified specific circuit constitution to an A/D converter.

CONSTITUTION: Comparators 11 whose one input terminals a reference voltage is inputted to, a ladder resistor 12 which consists of resistances 12 connected in series between two potentials and where connection points between resistances 12 are connected to input terminals of comparators, a signal input terminal 13 to which an analog signal is inputted, and coupling capacitors 14 which are arranged between the signal input terminal 13 and respective connection points of the ladder resistor 12 are provided. Since one reference voltage inputted to one terminals of plural comparators 11a to 11c is enough, the offset accompanied with the variance of plural reference voltages does not occur, and the reference level of A/D conversion is easily set. Even if the offset occurs in the analog input signal, it is eliminated by coupling capacitors 14.

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